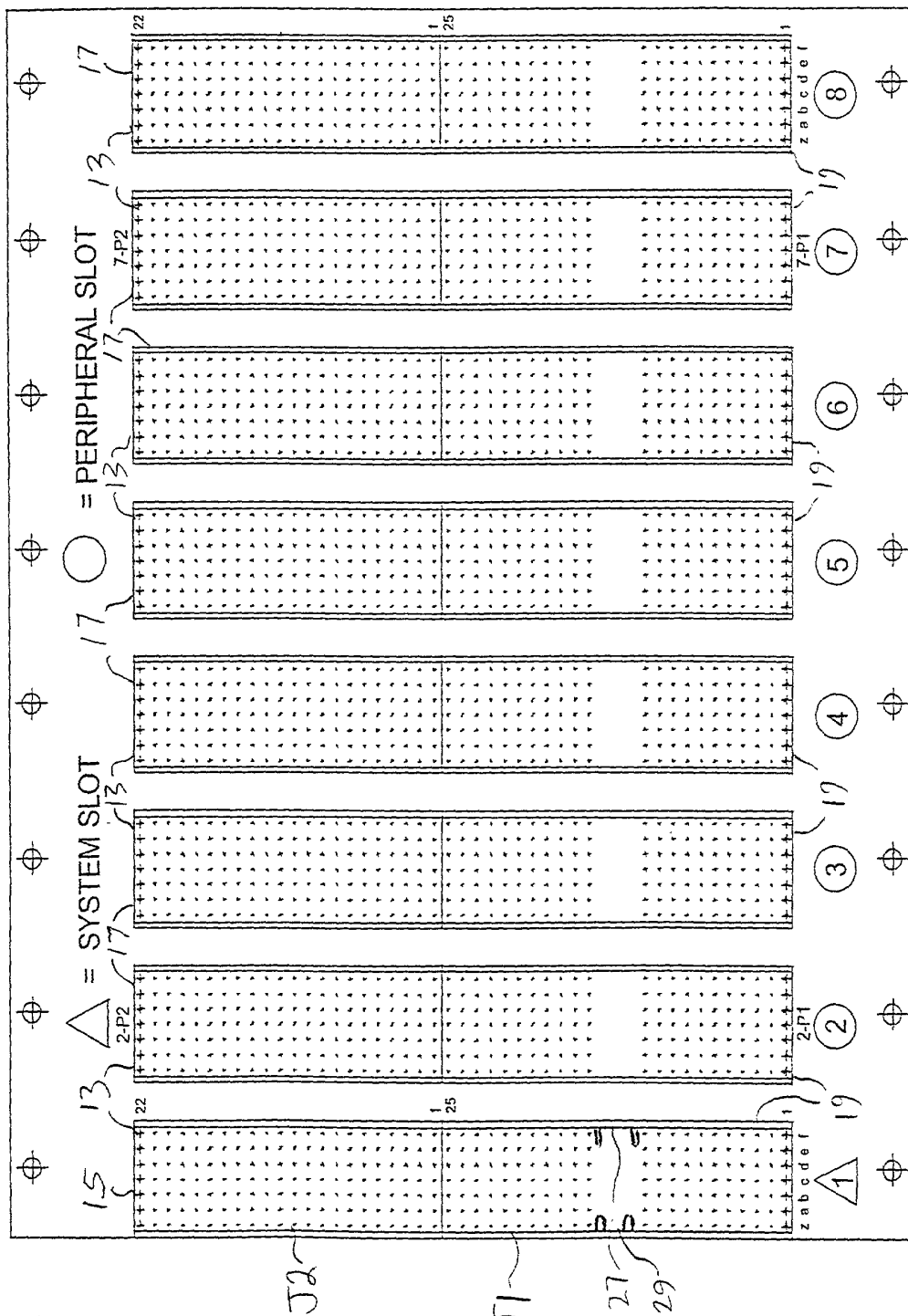


FIG. 1 is a schematic diagram of a system slot and a peripheral slot in a circuit board.



(Prior Art)

FIG. 1

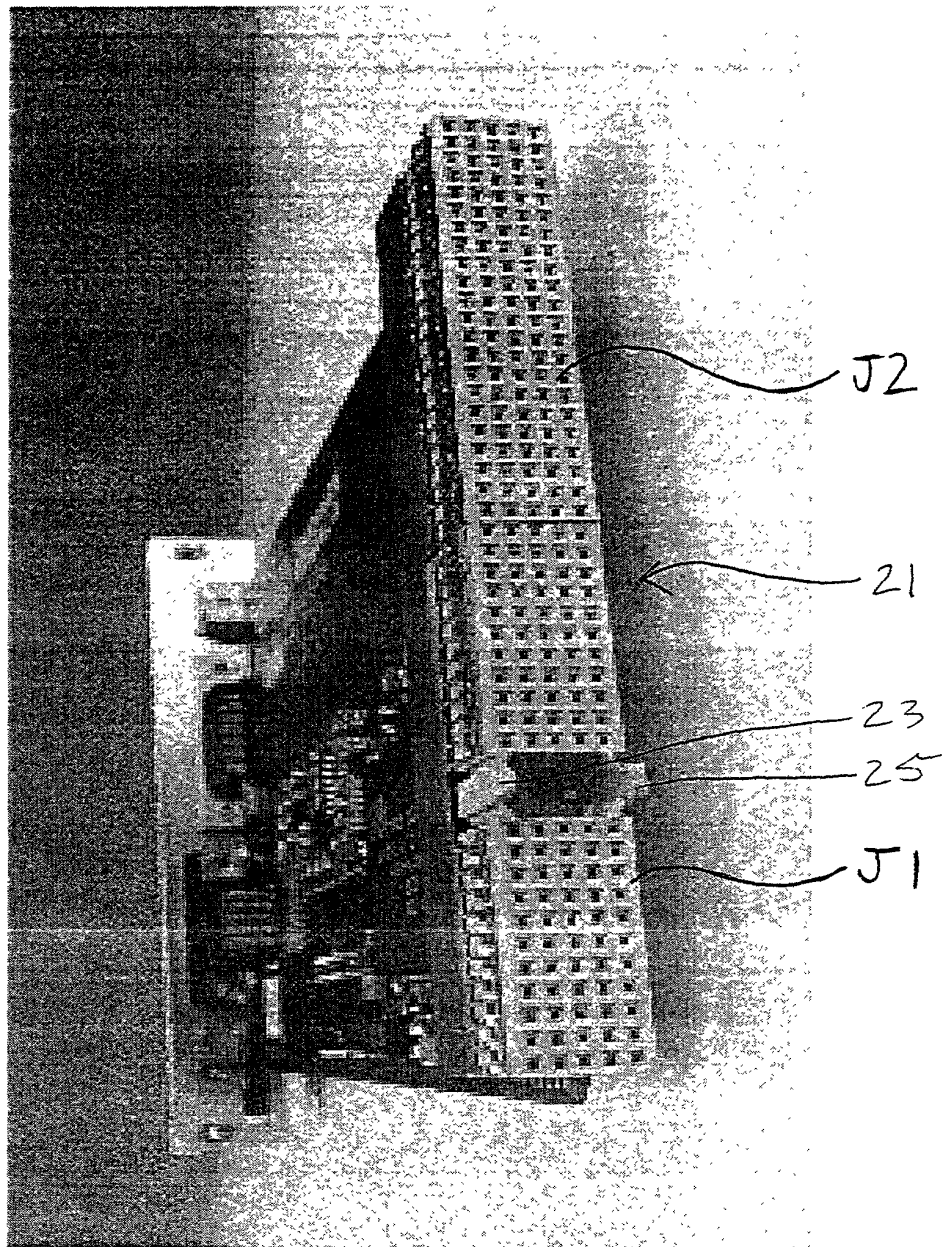
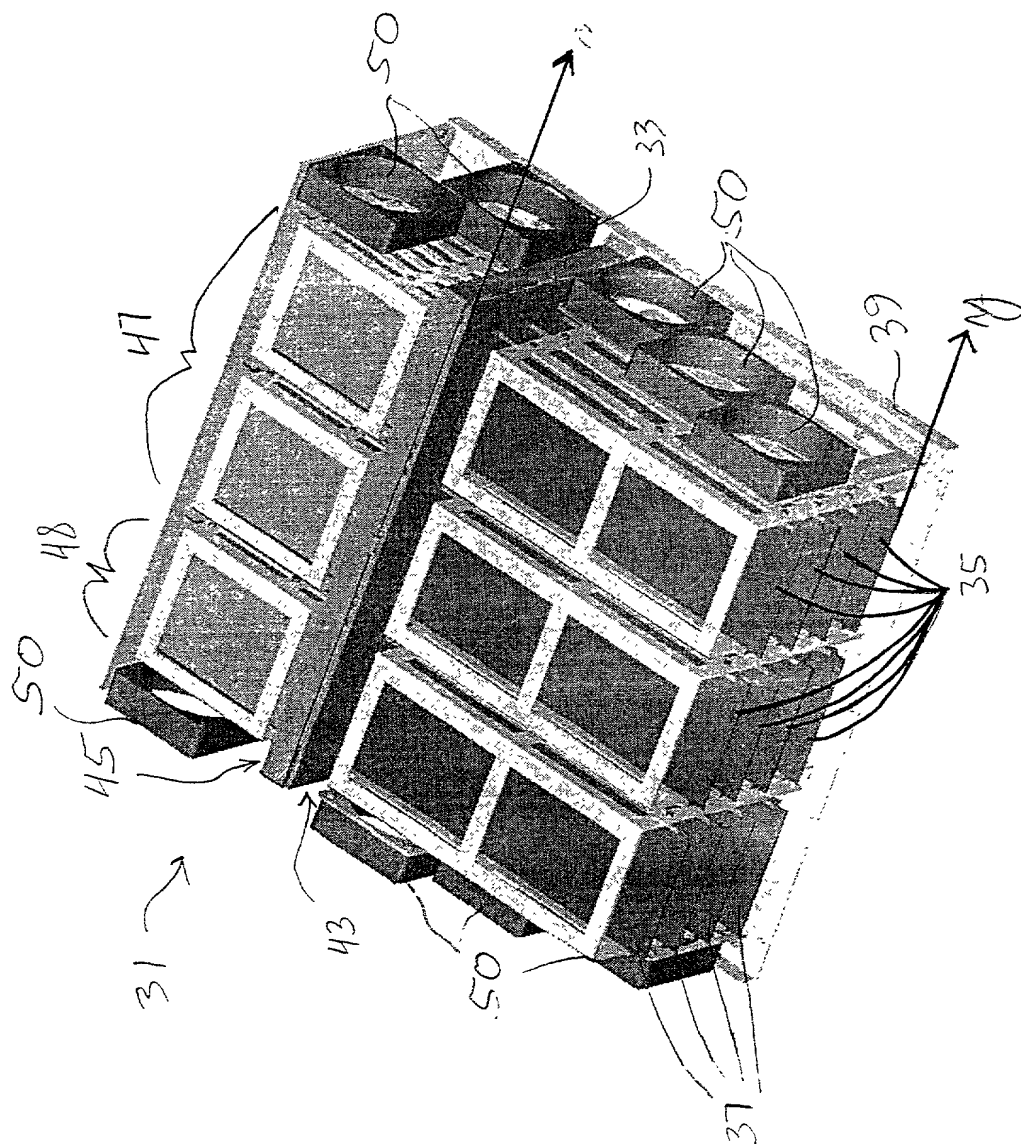


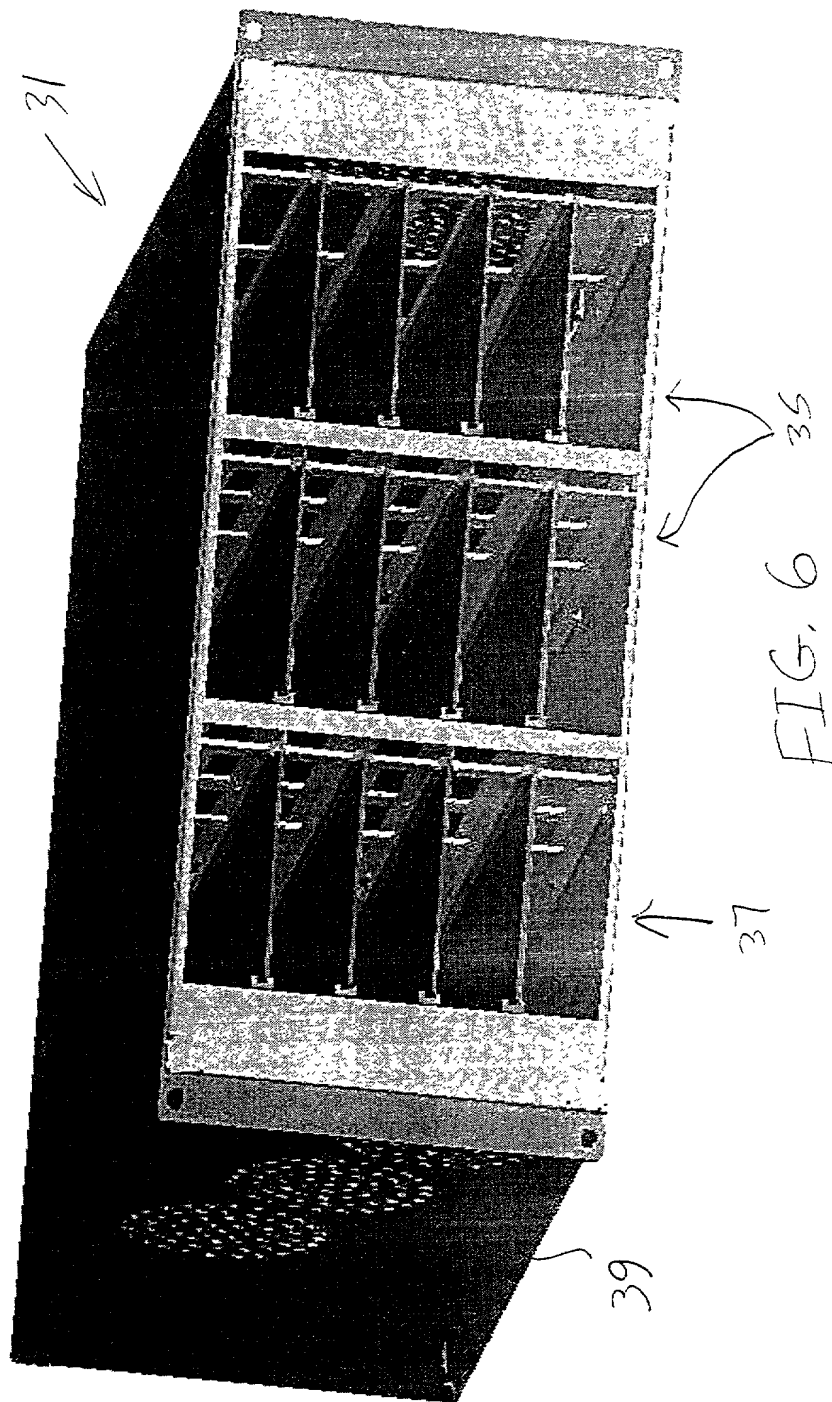
FIG. 2 (Prior Art)

25	GND	5V	REQ64#	ENUM#	3.3V	5V	GND	J1
24	GND	AD[1]	5V	V(I/O)	AD[0]	ACK64#	GND	
23	GND	3.3V	AD[4]	AD[3]	5V	AD[2]	GND	
22	GND	AD[7]	GND	3.3V	AD[6]	AD[5]	GND	
21	GND	3.3V	AD[9]	AD[8]	M66EN	C/BE[0]#	GND	
20	GND	AD[12]	GND	V(I/O)	AD[11]	AD[10]	GND	
19	GND	3.3V	AD[15]	AD[14]	GND	AD[13]	GND	
18	GND	SERR#	GND	3.3V	PAR	C/BE[1]#	GND	
17	GND	3.3V	SDONE	SBO#	GND	PERR#	GND	
16	GND	DEVSEL#	GND	V(I/O)	STOP#	LOCK#	GND	
15	GND	3.3V	FRAME#	IRDY#	GND	TRDY#	GND	
12-14	KEY AREA							C O N N E C T O R
11	GND	AD[18]	AD[17]	AD[16]	GND	C/BE[2]#	GND	
10	GND	AD[21]	GND	3.3V	AD[20]	AD[19]	GND	
9	GND	C/BE[3]#	IDSEL	AD[23]	GND	AD[22]	GND	
8	GND	AD[26]	GND	V(I/O)	AD[25]	AD[24]	GND	
7	GND	AD[30]	AD[29]	AD[28]	GND	AD[27]	GND	
6	GND	REQ#	GND	3.3V	CLK	AD[31]	GND	
5	GND	BRSVP1A5	BRSVP1B5	RST#	GND	GNT#	GND	
4	GND	BRSVP1A4	GND	V(I/O)	INTP	INTS	GND	
3	GND	INTA#	INTB#	INTC#	5V	INTD#	GND	
2	GND	TCK	5V	TMS	TDO	TDI	GND	
1	GND	5V	-12V	TRST#	+12V	5V	GND	
Pin	Z	A	B	C	D	E	F	

FIG. 4



5165



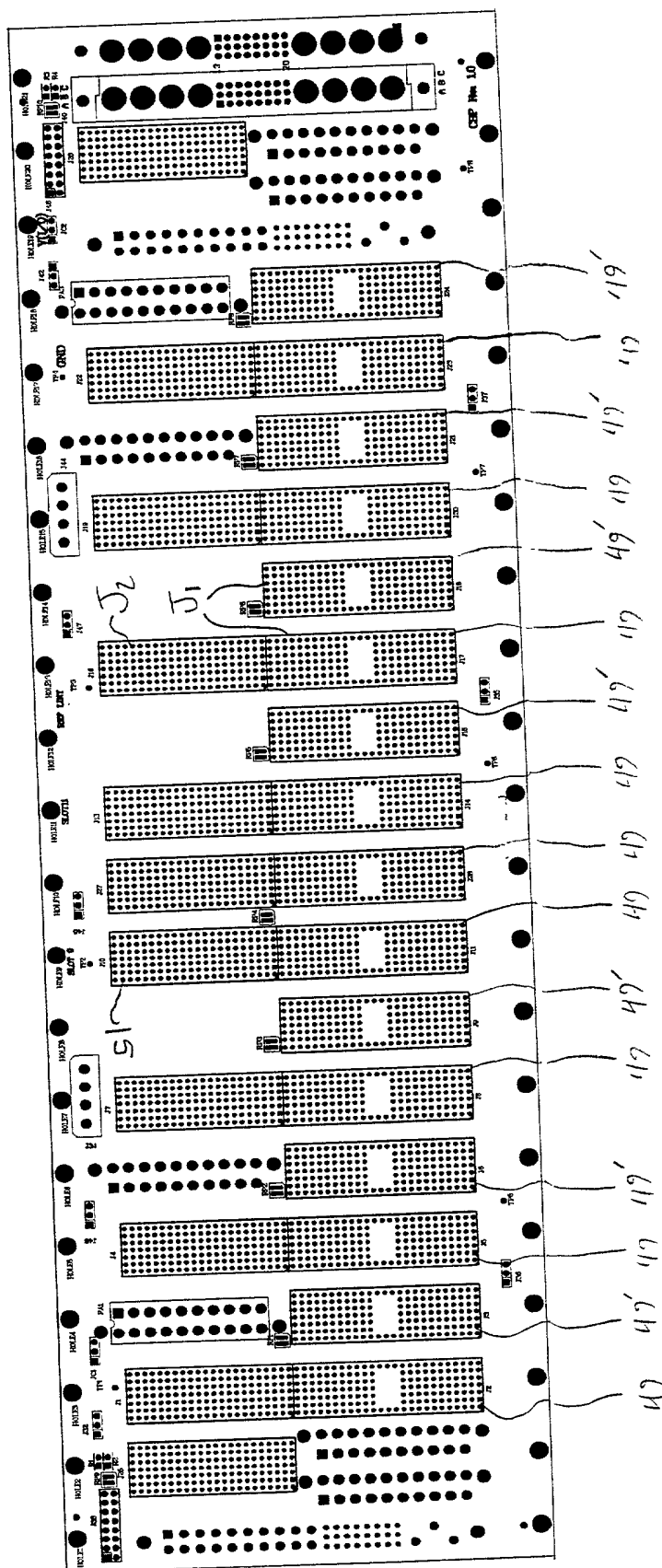


FIG. 7

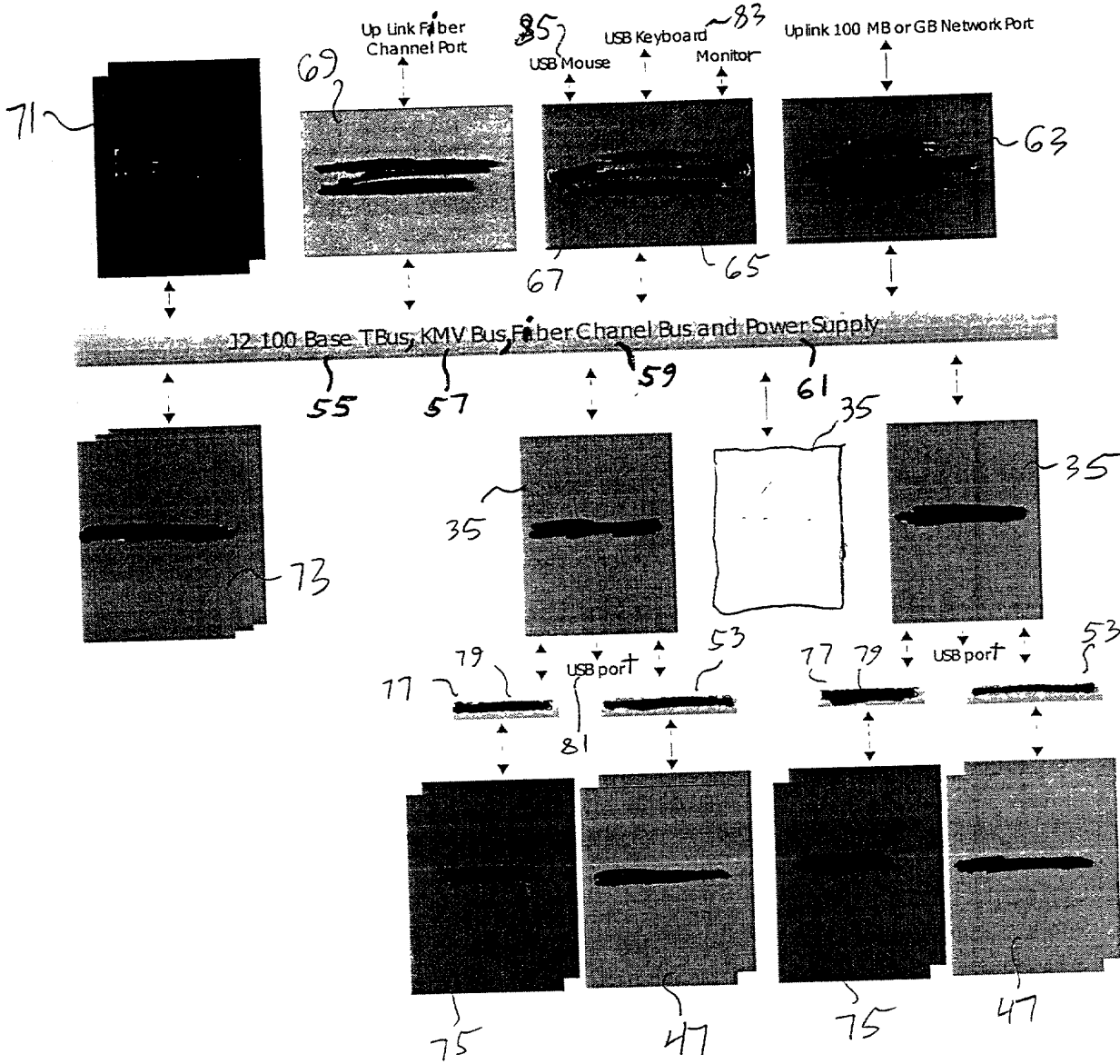


FIG. 9

FIG. 10 is a block diagram of an E Server architecture. The diagram shows a top view of the server chassis with various components and their interconnections. The top view includes a Network Switch, 12 cPCI Expansion Slots, a Video Port, a USB Keyboard & Mouse Port, a KMV Switch LED, a cPCIHandle, an RS232 Management Port, and a Network Port. Below the top view, a side view shows the internal components: a KMV Switch, a 100 Base-T Management Switch, 12 cPCI Expansion Slots, and a cPCI Expansion Slot. These components are connected to a central cPCIMid Plane. Below the mid plane, there are three 300 W Power Supply units, 3 U-cPCI CPUs, and 3 U-cPCI GPUs. The bottom view shows the server's front panel with an Air Flow indicator, Power LED, Network LED, Network LED, CPU Work LED, Reset button, Power On/Off button, and Cooling Fan.

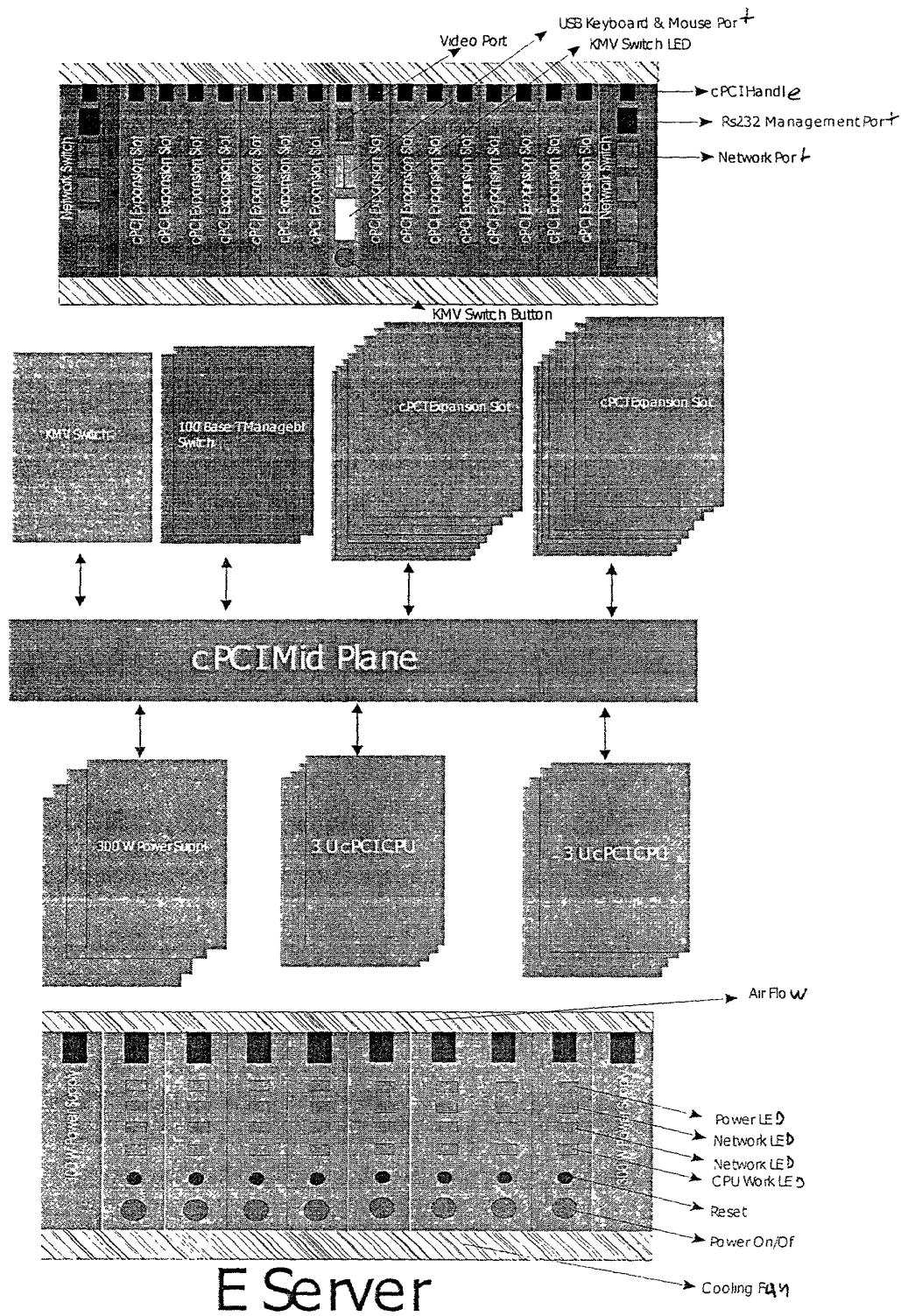


FIG. 10

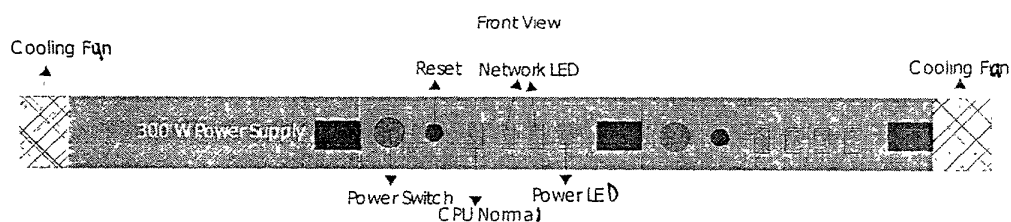
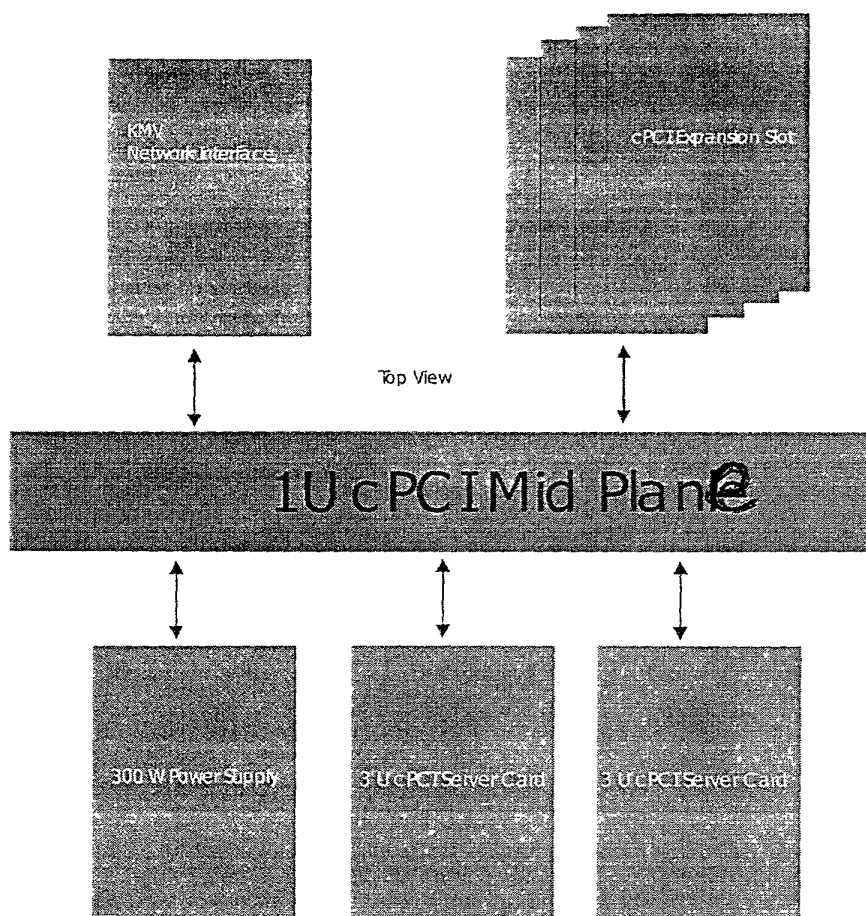
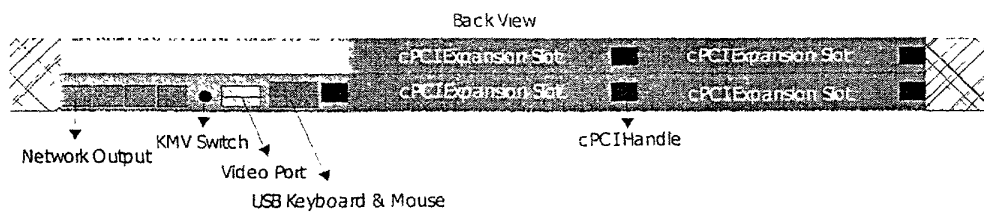


FIG. 11

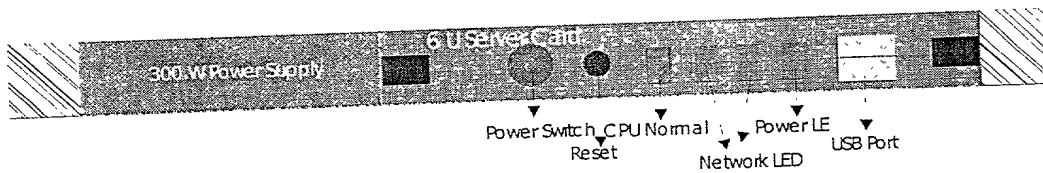
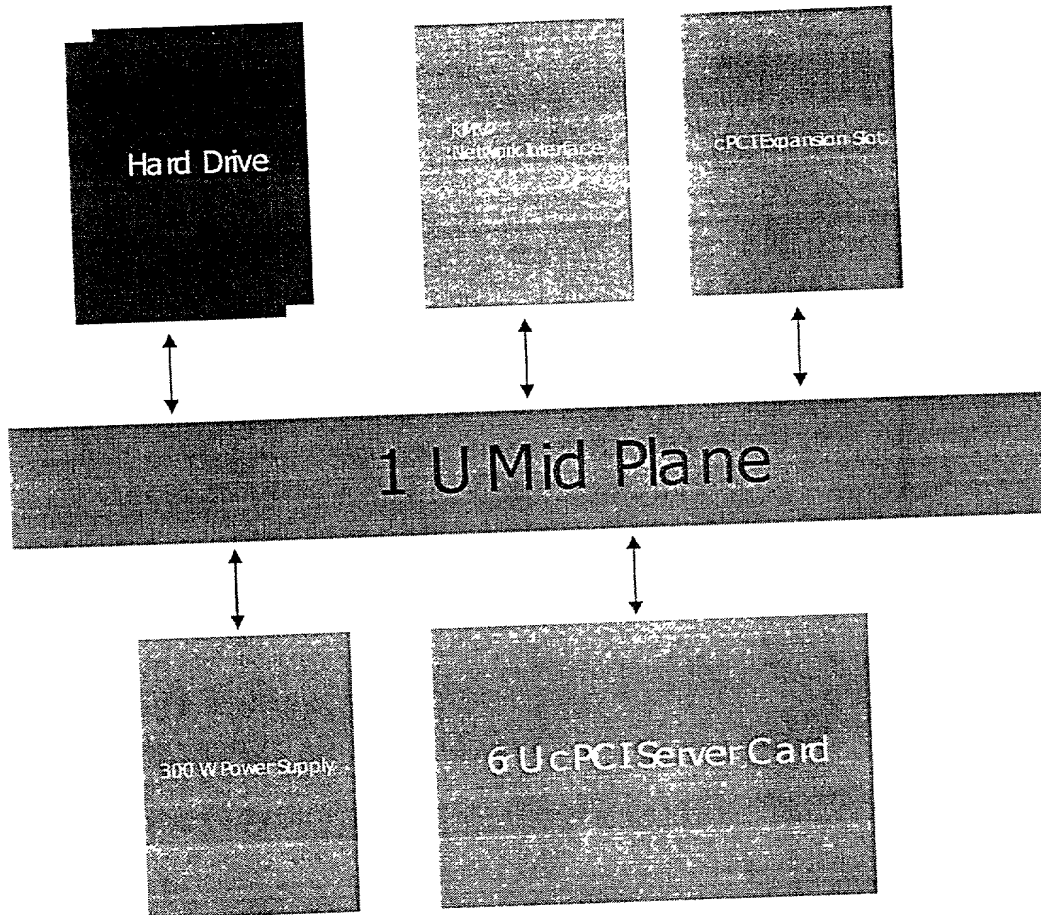


FIG. 12

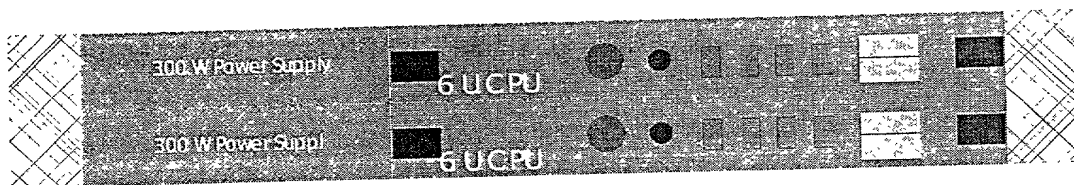
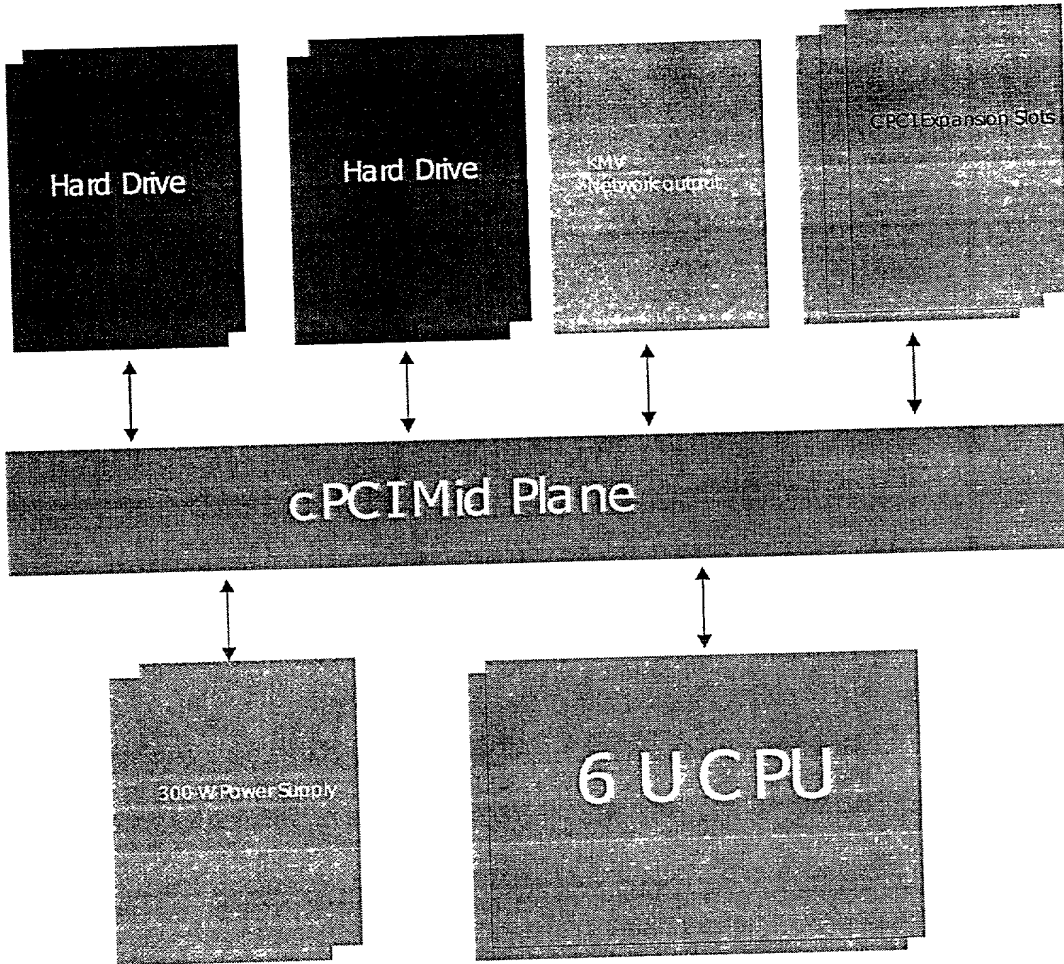
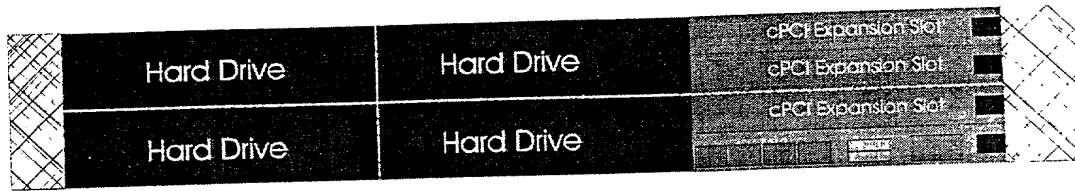


FIG. 13

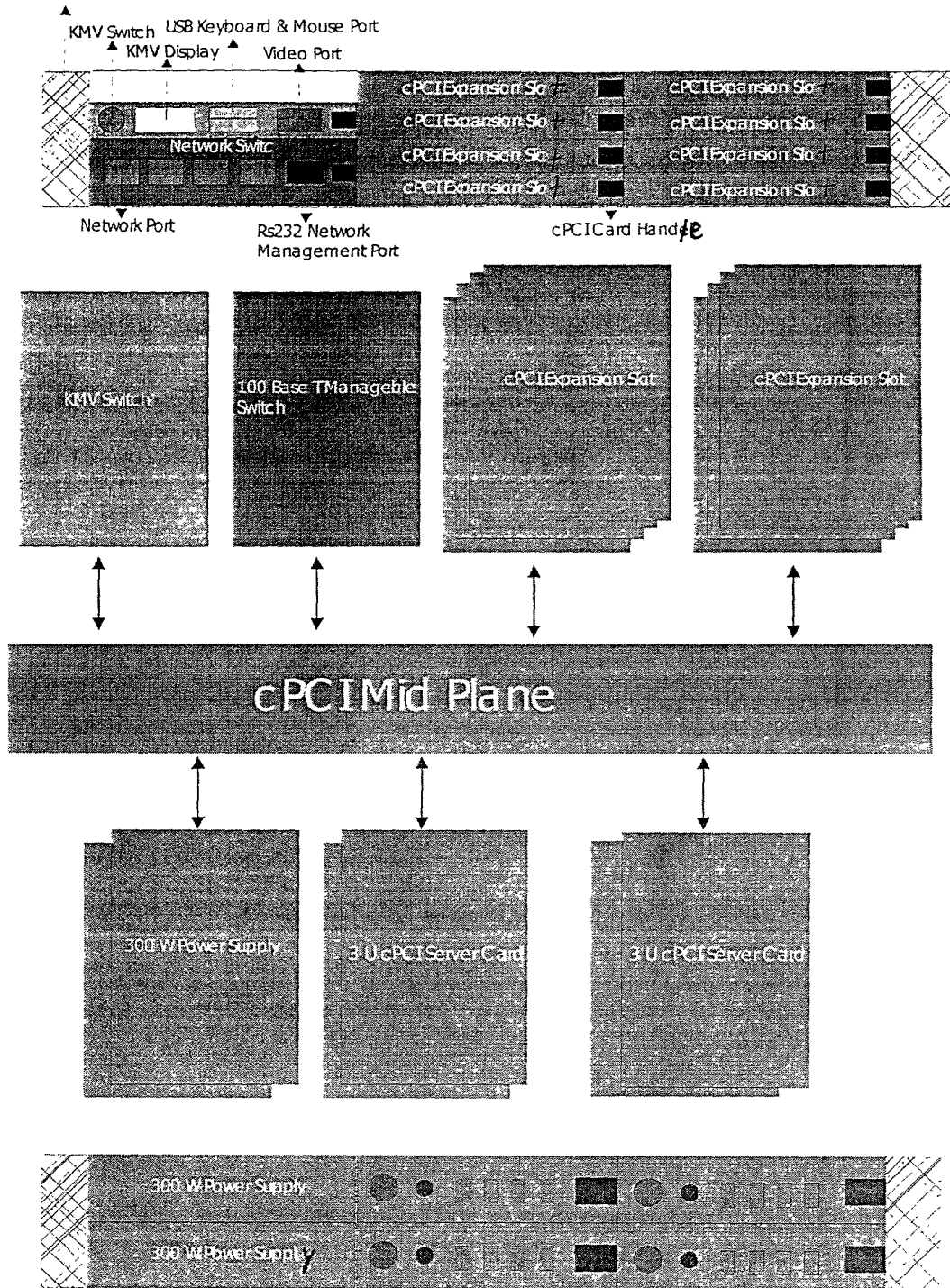


FIG. 14

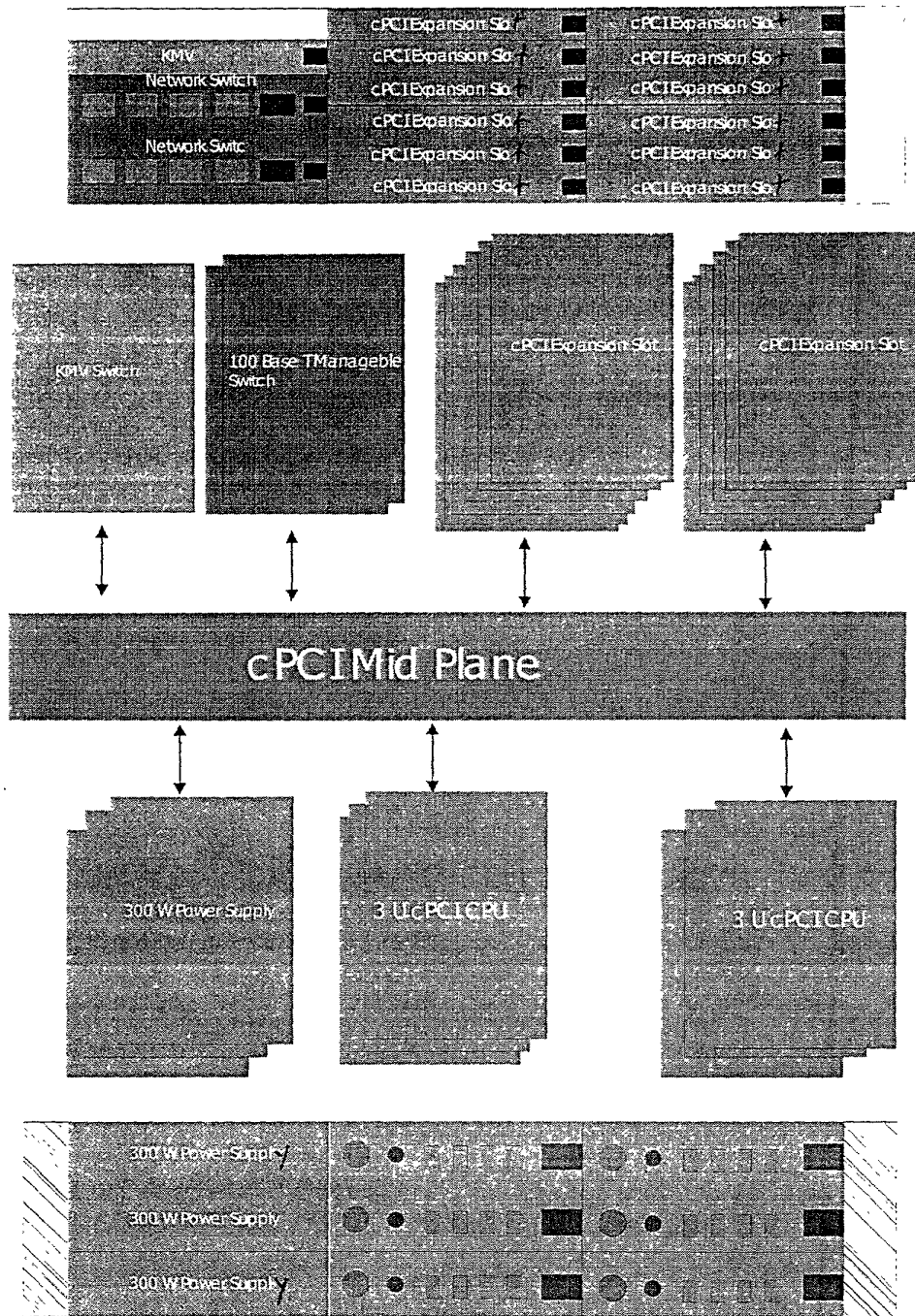


FIG. 15

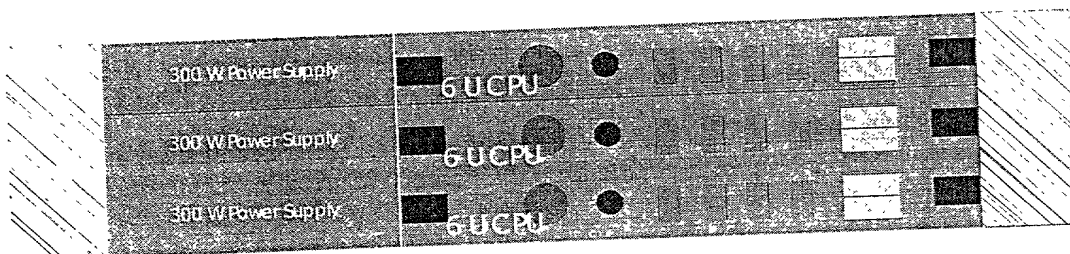
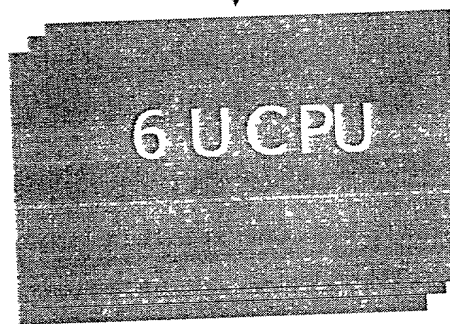
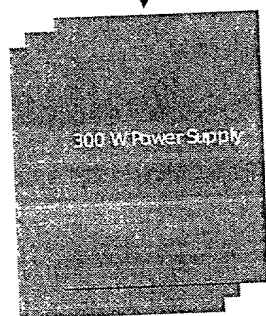
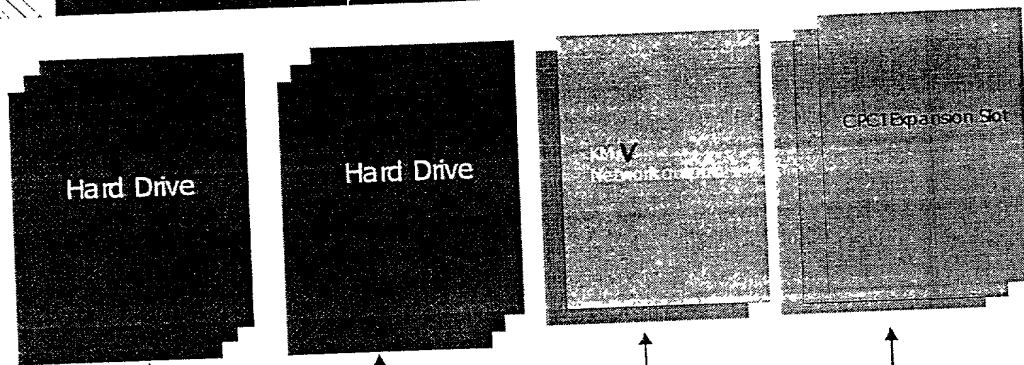
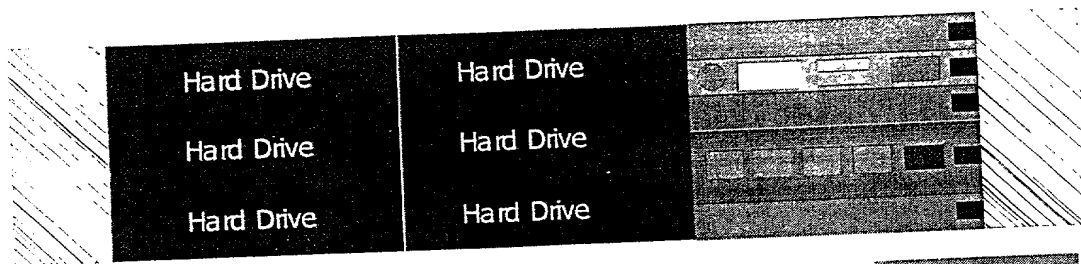


FIG. 16

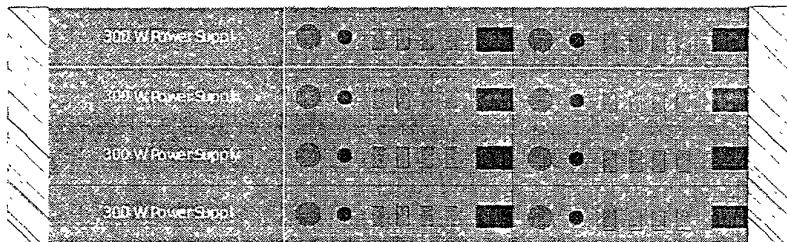
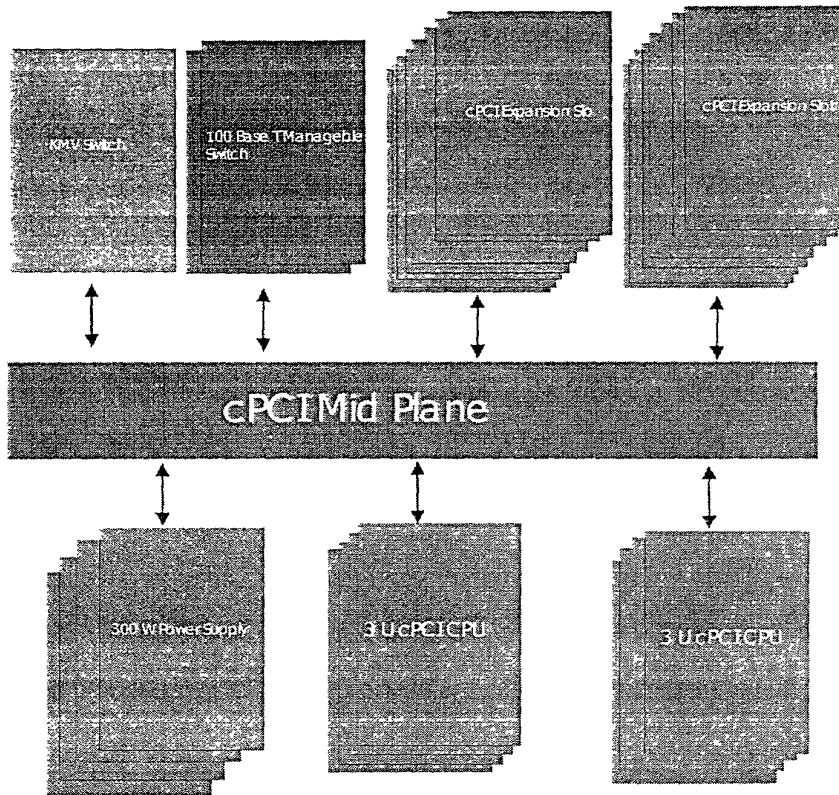
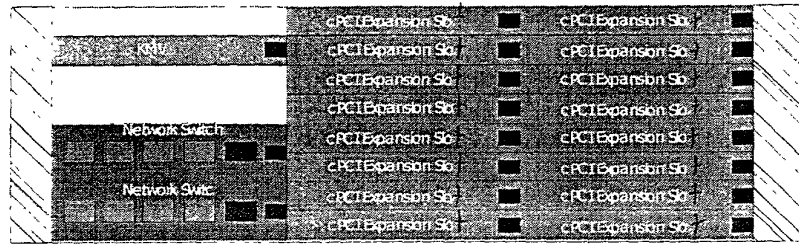


FIG. 17

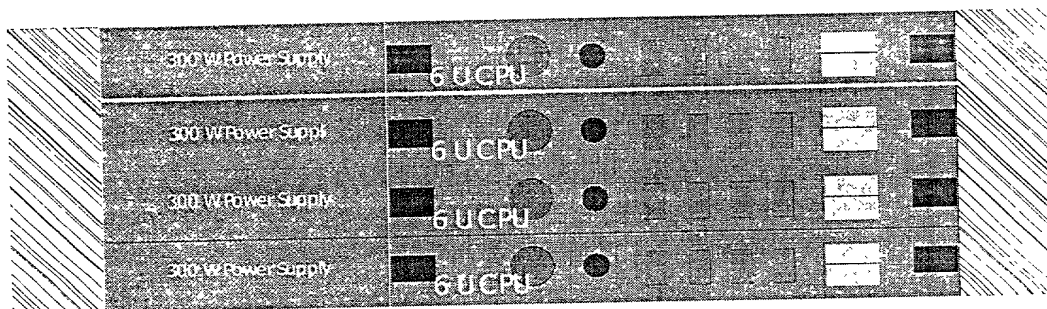
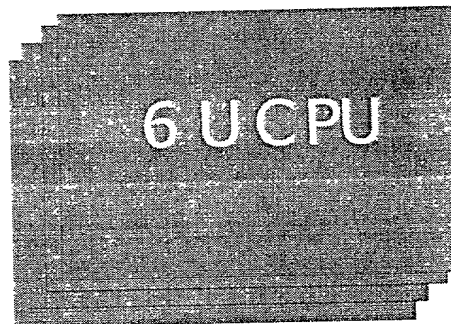
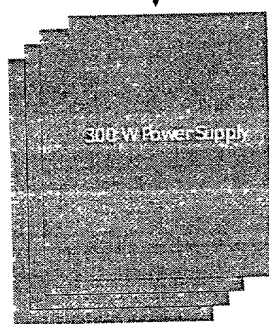
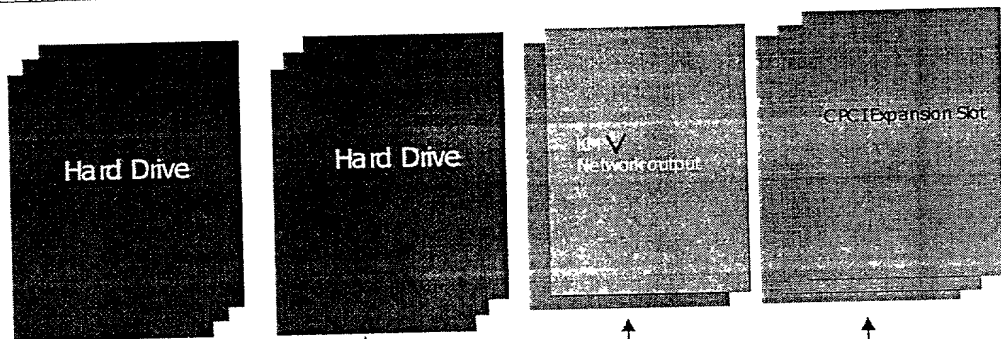
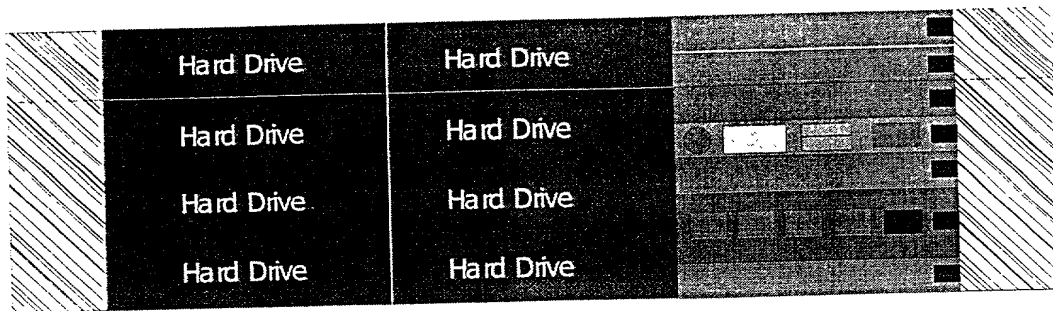


FIG. 18

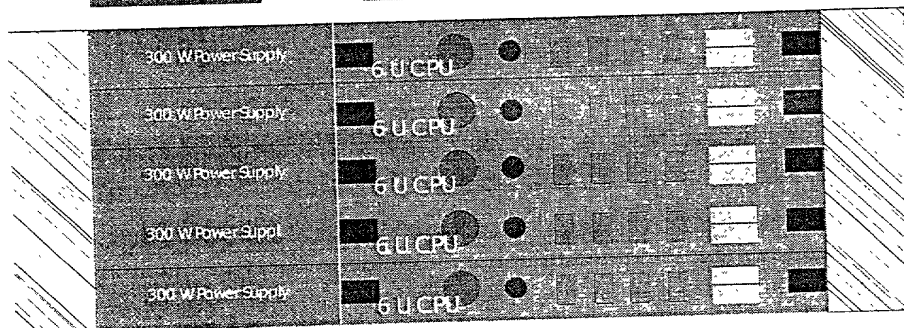
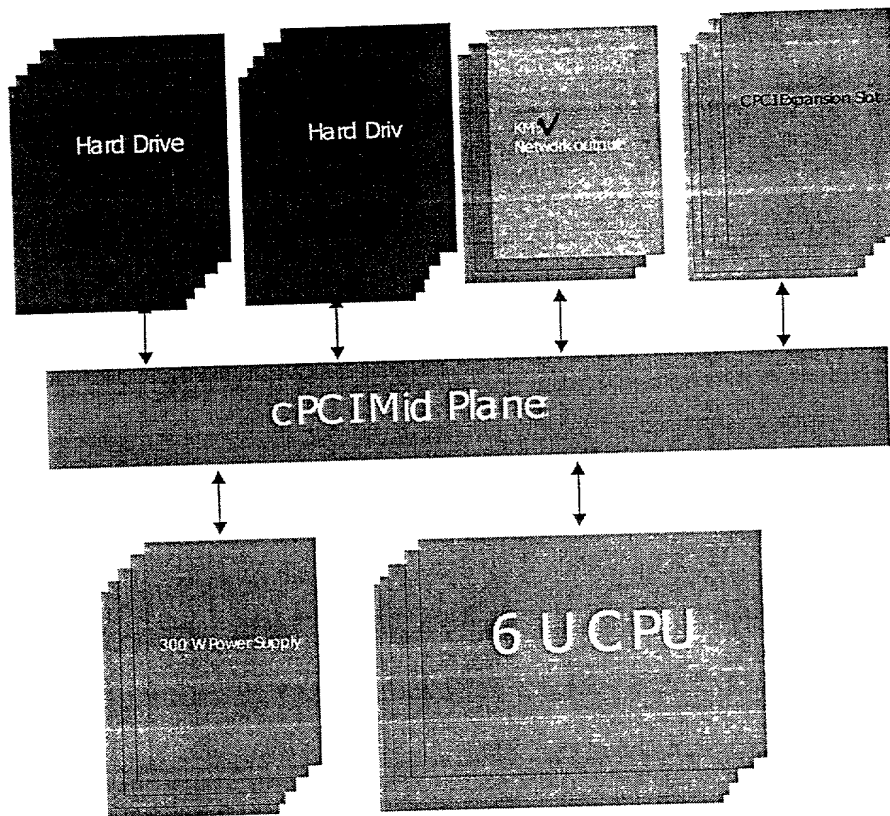
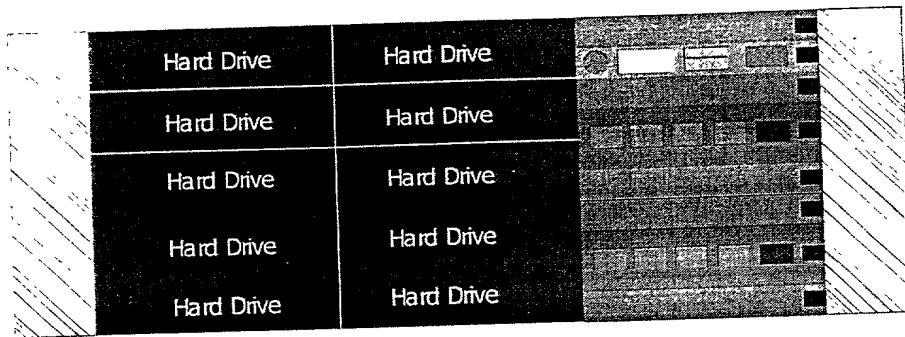


FIG. 19

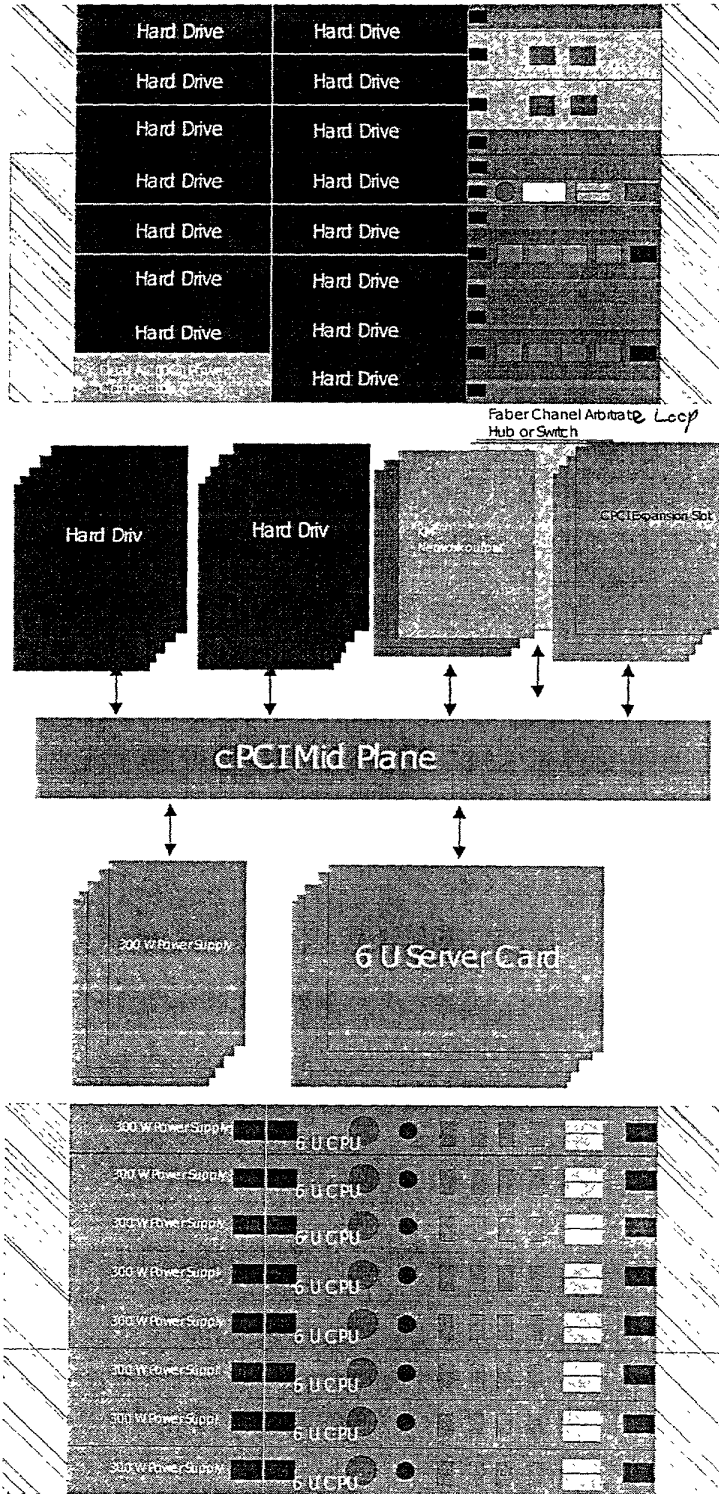


FIG. 20

Fig. 21

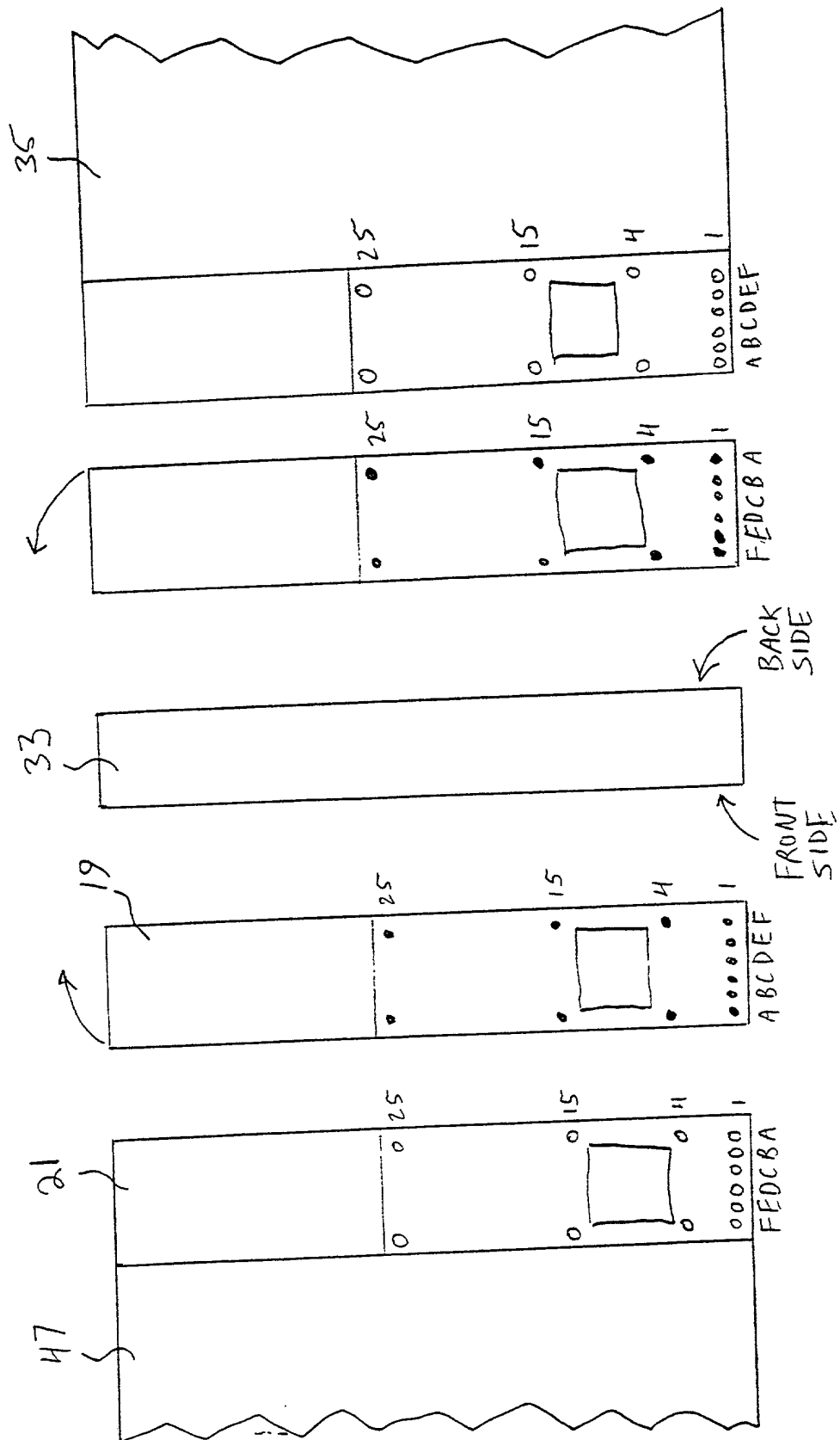


Figure 22 shows a plan view of a circuit board layout. The board is rectangular and contains various components including resistors (R1, R2, R3, R4, R5, R6, R7, R8, R9, R10, R11, R12, R13, R14, R15, R16, R17, R18, R19, R20), capacitors (C1, C2, C3, C4, C5, C6, C7, C8), and integrated circuits (J1, J2, J3, J4, J5, J6, J7, J8, J9, J10, J11, J12, J13, J14, J15, J16, J17, J18, J19, J20). The components are arranged in a grid-like pattern. The board dimensions are indicated as 6299.213 (mil) by 3937 (mil). The board is labeled with 'FEDCBA' along the top edge and '1 11 15 25' along the right edge. A handwritten note '63 21' with an arrow points to the right edge of the board.

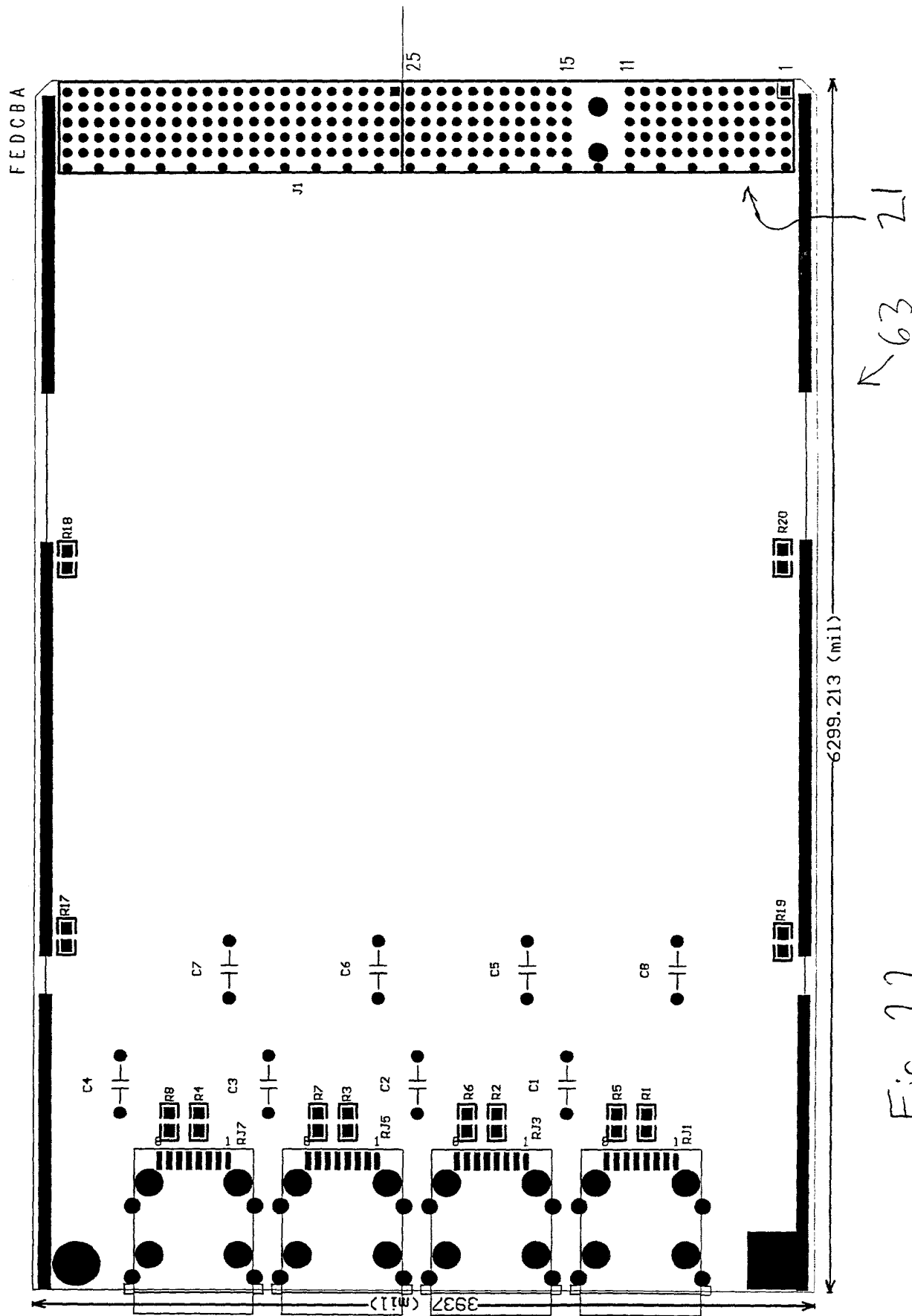


Fig. 22

FIG. 23 is a schematic diagram of a system for monitoring a process. The system includes a process 10, a sensor 12, a controller 14, and a display 16. The sensor 12 is connected to the controller 14, which is connected to the display 16. The controller 14 is also connected to the process 10. The display 16 displays information about the process 10.

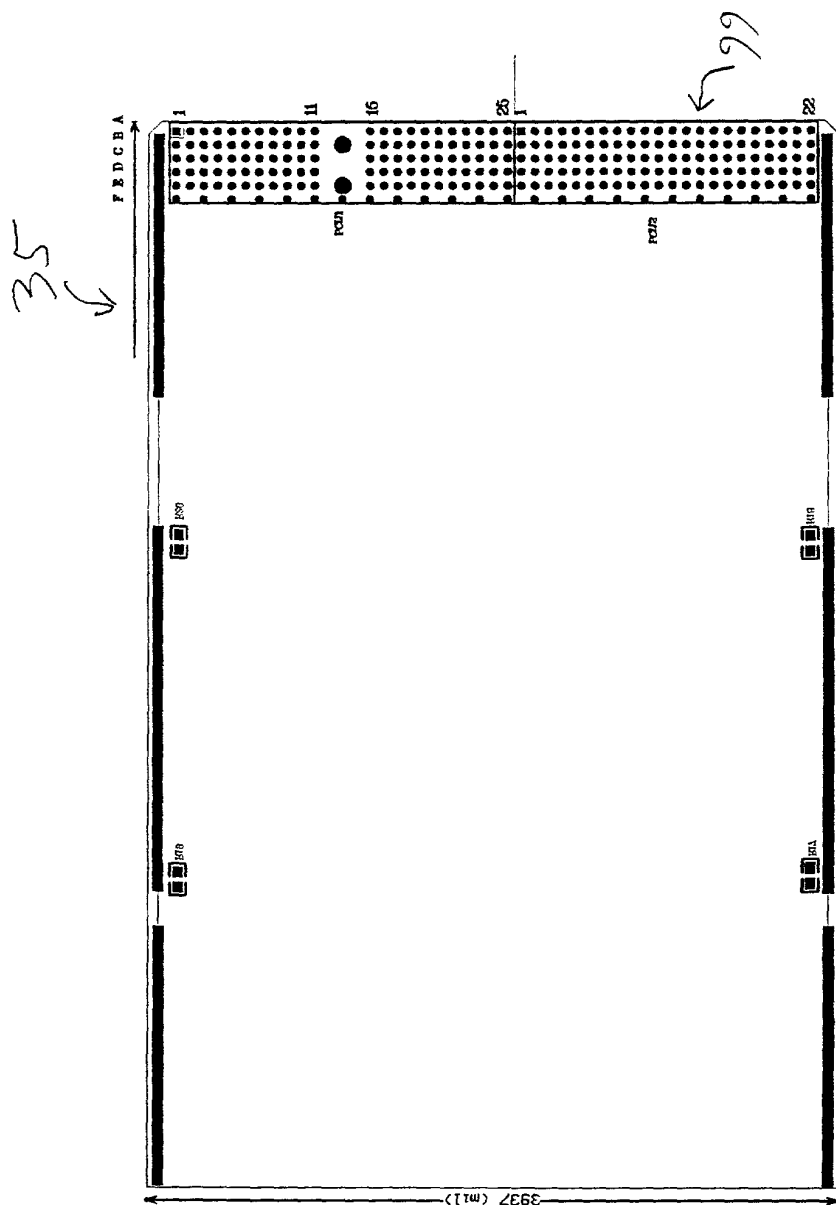


Fig. 23

22	GND	GND	GND	GND	GND	GND	GND	J2
21	GND	?3	GND	?18	?24	SMBDAT	GND	
20	GND	?2	?7	?17	GND	SMCLK	GND	
19	GND	GND	GND	?15	?22	CUV1+	GND	
18	GND	MDCLK	?5	?14	GND	GND	GND	C
17	GND	GND	GND	?13	?21	CUV1-	GND	
16	GND	MDDAT	MVSYNC	?12	GND	GND	GND	
15	GND	GND	GND	?11	?20	?28	GND	
14	GND	RSTSW#	MHSYNC	VCC5IN	GND	?27	GND	O
13	GND	GND	GND	VIO	ER1+	GND	GND	
12	GND	CUV0-	MB	VCC5IN	GND	ER2+	GND	
11	GND	GND	GND	VIO	ER1-	GND	GND	
10	GND	CUV0+	MG	VCC5IN	GND	ER2-	GND	E
9	GND	GND	GND	VIO	ET1+	GND	GND	
8	GND	MUSDATA	MR	VCC5IN	GND	ET2+	GND	
7	GND	GND	GND	VIO	ET1-	GND	GND	
6	GND	MUSCLK	?10	VCC5IN	GND	ET2-	GND	T
5	GND	GND	?9	VIO	?26	GND	GND	
4	GND	VIO	?8	VCC5IN	GND	CUV3-	GND	
3	GND	?4	GND	?19	?25	GND	GND	
2	GND	?1	?6	?16	?23	CUV3+	GND	R
1	GND	PCICLK4	GND	PREQ# 3	PGNT# 3	GND	GND	
Pin	Z	A	B	C	D	E	F	

FIG. 24